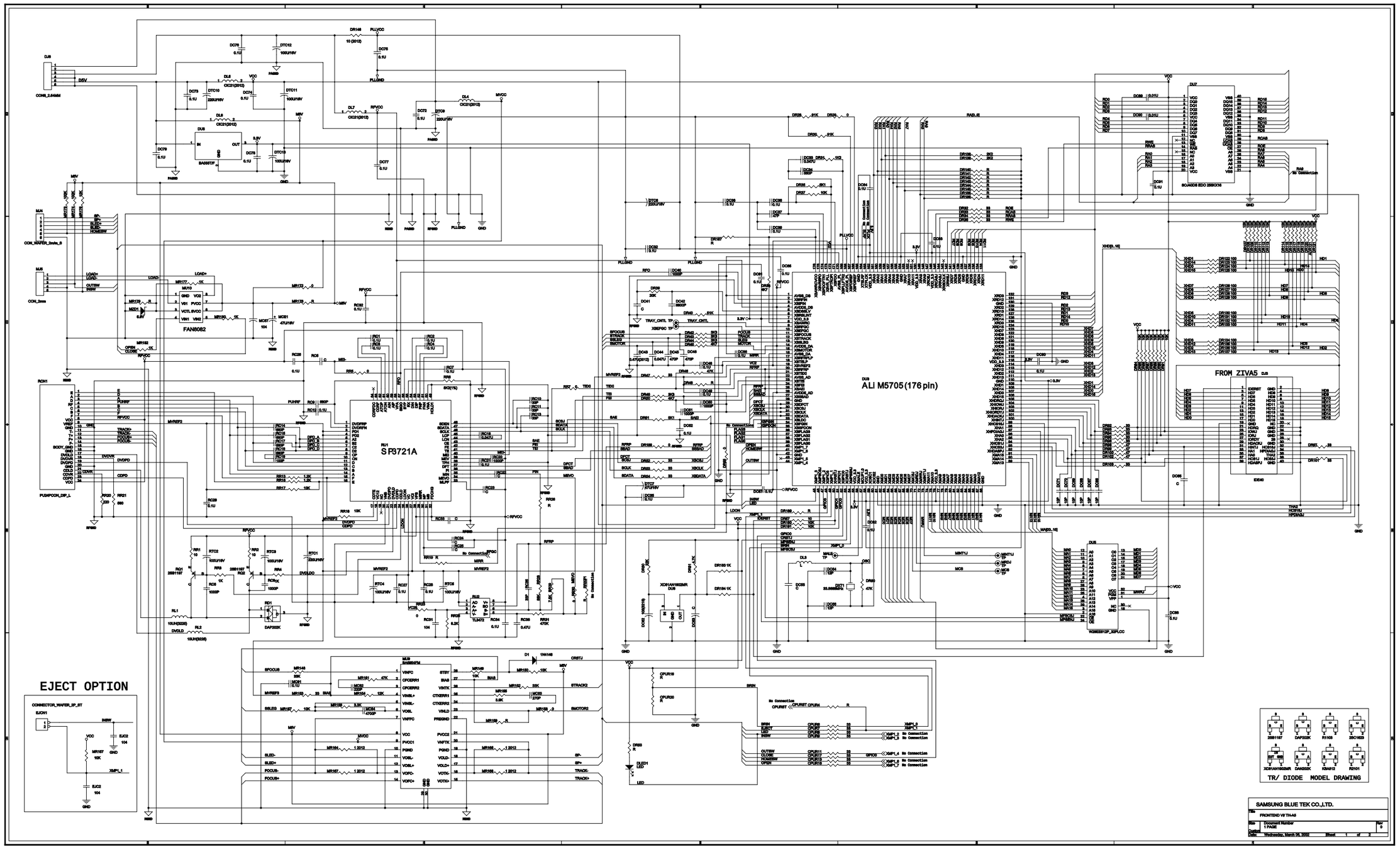
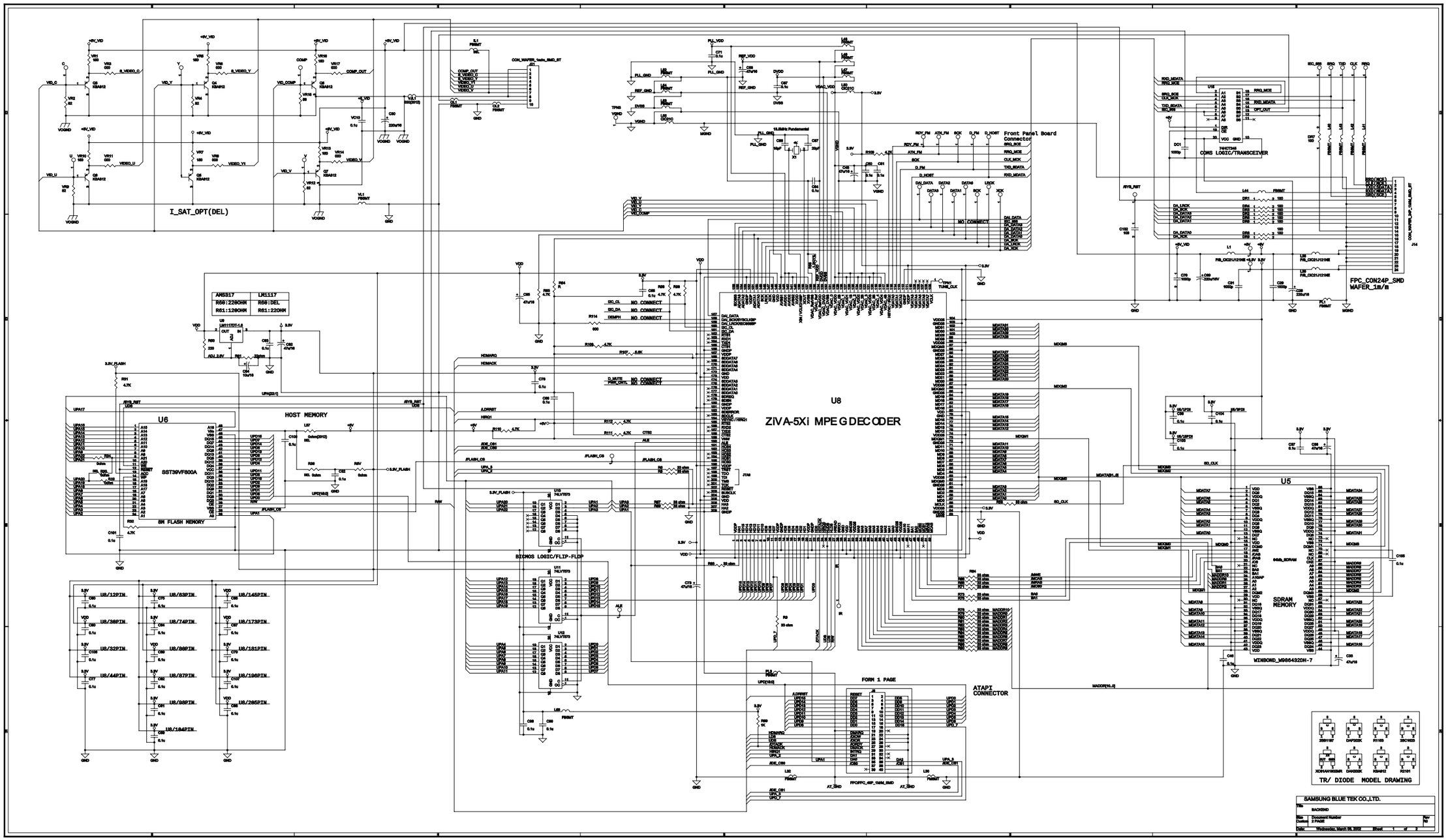
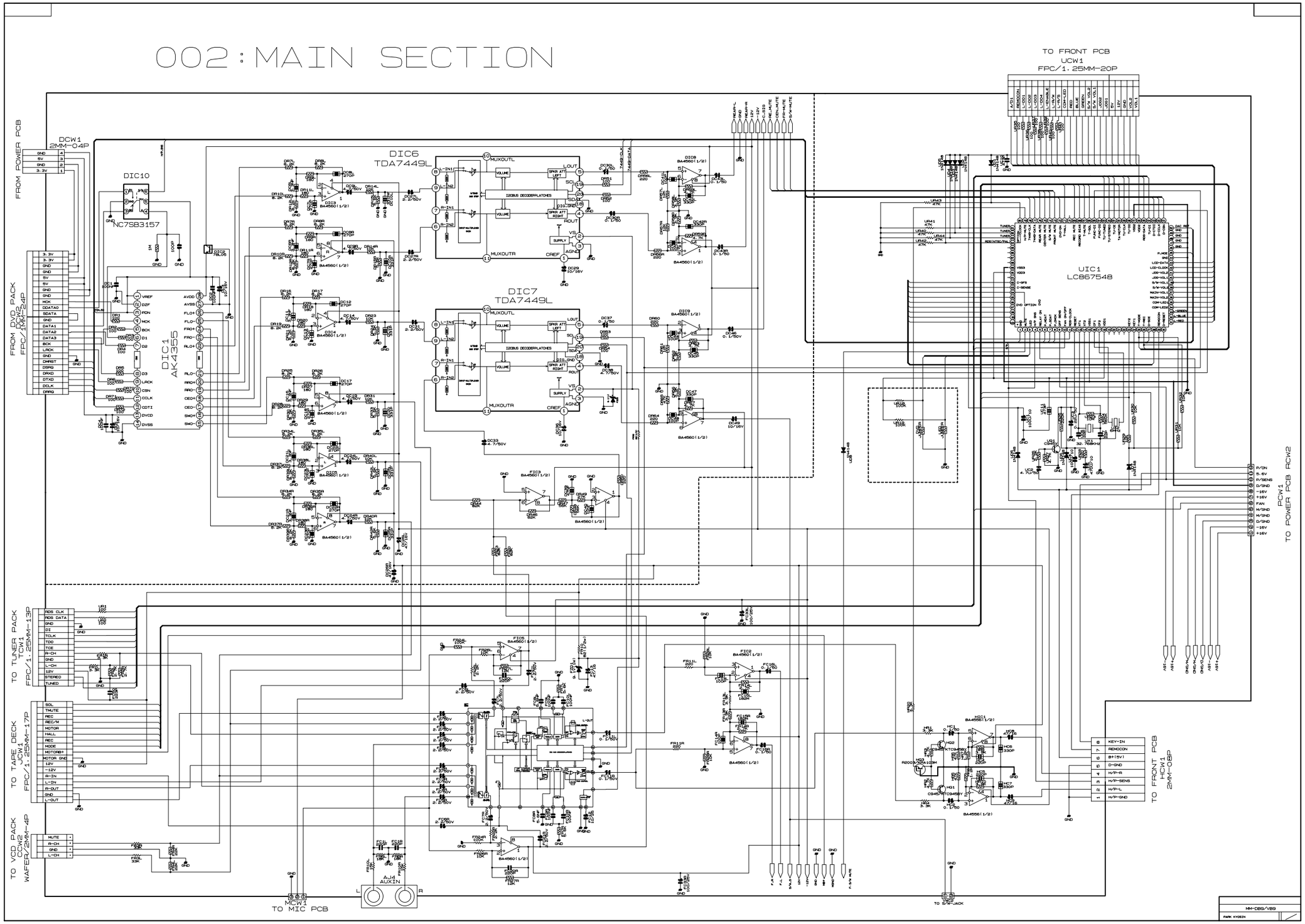
7. Schematic Diagram

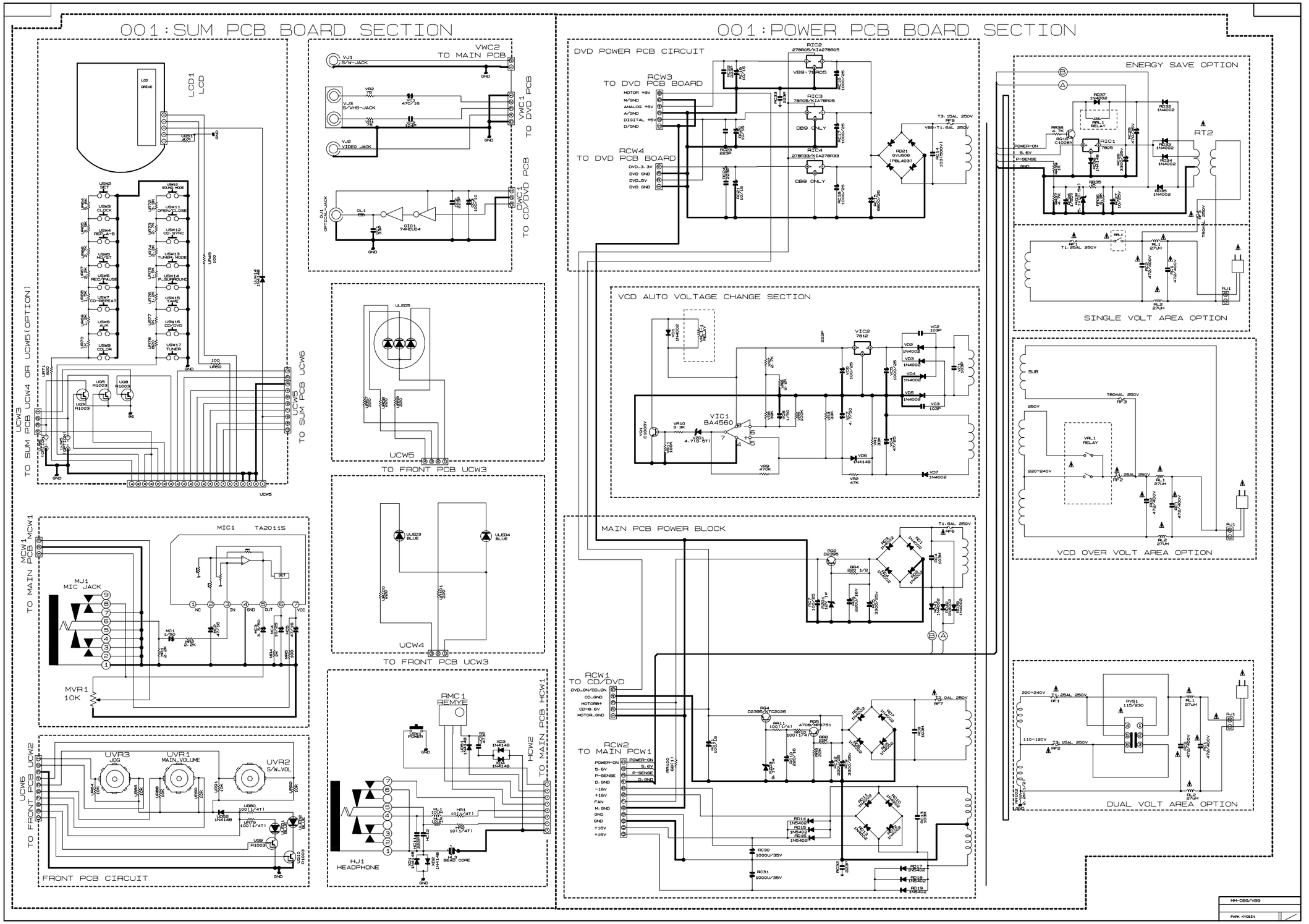
7-1 DVD Frontend Part

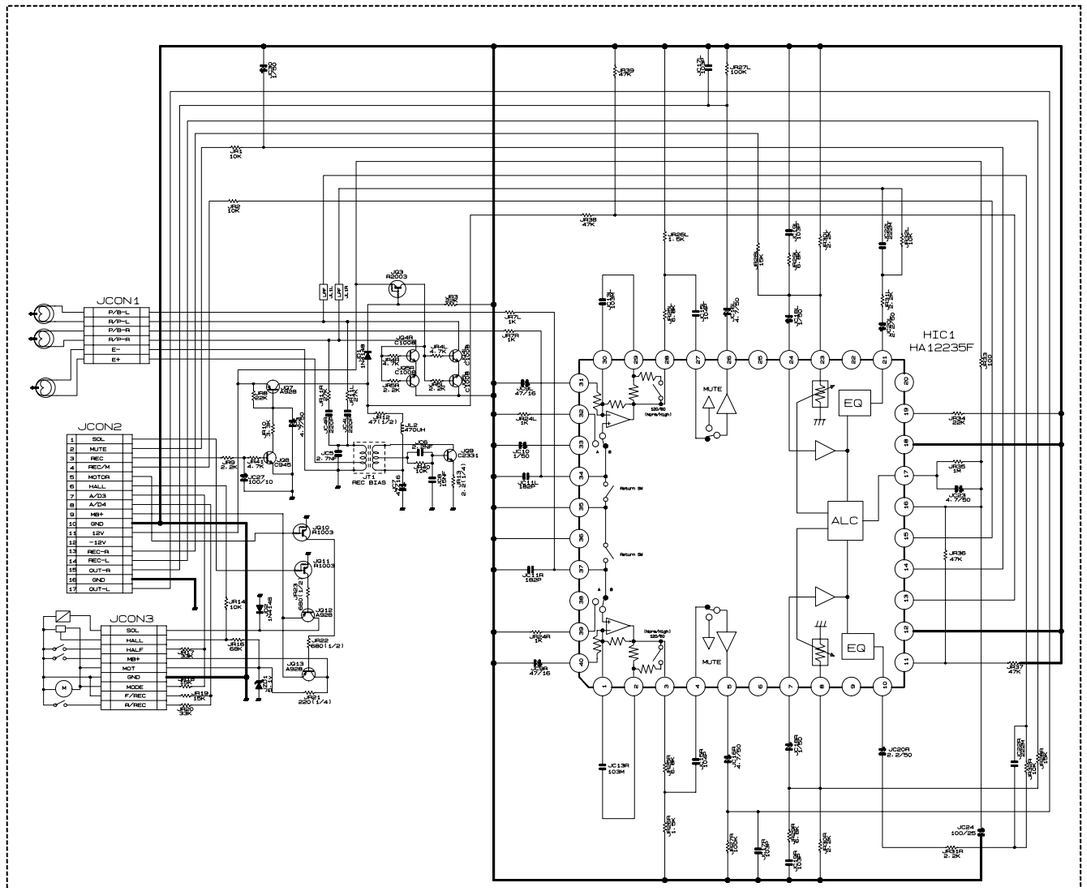


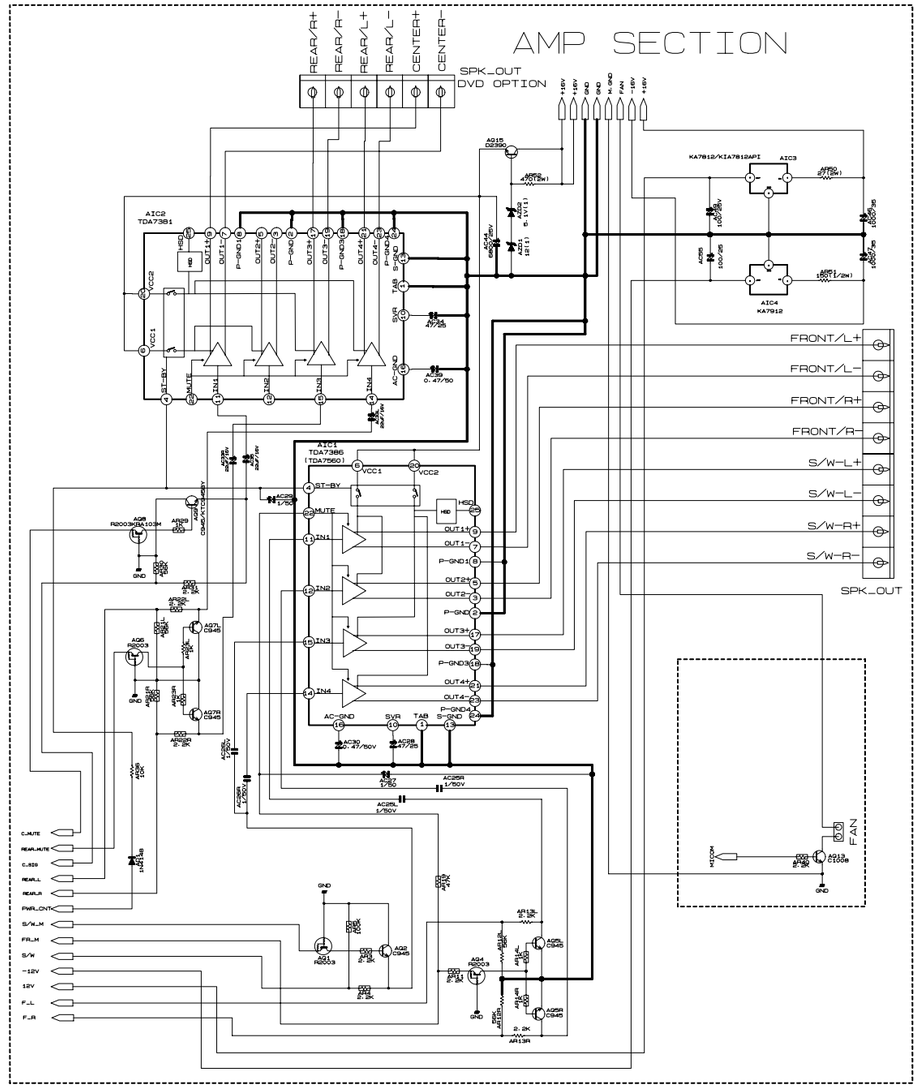

U8
ZiVA-5Xi MPEG DECODER
U6
SST39VF800A
8M FLASH MEMORY
HOST MEMORY
BICMOS LOGIC/FLIP-FLOP
U5
SDRAM MEMORY
WINBOND_W986432DH-7
ATAPI CONNECTOR
FORM 1 PAGE
Front Panel Board Connector
CDMS LOGIC/TRANSCEIVER
FPC_CON24P_SMD WAFER_1m/m
I_SAT_OPT(DEL)
TR/ DIODE MODEL DRAWING
SAMSUNG BLUE TEK CO.,LTD.
BACKEND

002 : MAIN SECTION




001:SUM PCB BOARD SECTION
001:POWER PCB BOARD SECTION
LCD1 LCD
VWC2 TO MAIN PCB
VWC1 TO DVD PCB
CWC1 TO CD/DVD PCB
UCW3 TO SUM PCB UCW4 OR UCW5(OPTION)
UCW5 TO SUM PCB UCW6
UCW5 TO FRONT PCB UCW3
UCW4 TO FRONT PCB UCW3
MCW1 TO MAIN PCB MCW1
UCW6 TO FRONT PCB UCW2
HCW2 TO MAIN PCB HCW1
FRONT PCB CIRCUIT
HJ1 HEADPHONE
DVD POWER PCB CIRCUIT
RCW3 TO DVD PCB BOARD
RCW4 TO DVD PCB BOARD
VCD AUTO VOLTAGE CHANGE SECTION
MAIN PCB POWER BLOCK
RCW1 TO CD/DVD
RCW2 TO MAIN PCW1
ENERGY SAVE OPTION
SINGLE VOLT AREA OPTION
VCD OVER VOLT AREA OPTION
DUAL VOLT AREA OPTION
MIC1 TA2011S
MJ1 MIC JACK
MVR1 10K
UVR3 JOG
UVR1 MAIN_VOLUME
UVR2 S/W_VOL
RMC1 REMYE
MM-DB6/VB9
PARK KYOSIN





003 : AMP & TAPE SECTION

MM-DB9/VB9
K.S..PARK